

# RISC-V Foundation Security Standing Committee Call to action !!

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**Rambus**  
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# A security standing committee for the RISC-V Foundation

- RISC-V Foundation; a non-profit corporation controlled by its members
- 115+ member organizations already (companies, universities, individuals)
- **drive forward the adoption and implementation of the free and open RISC-V instruction set architecture (ISA)**
  - authored a.o. by *David Patterson, 2017 Turing Award winner*
- open source RISC-V ISA and draft Privilege Specs
- created a dedicated **Security Standing Committee**
  - identify potential security improvements for RISC-V based IoT devices, embedded systems, and machine learning implementations.
  - includes 30+ RISC-V Foundation members, including Berkeley Architecture Group, Bluespec, CSIRO's Data61, Dover, Draper, Esperanto Technologies, Indian Institute of Technology (IIT) Madras, Intrinsic ID, Galois, Hex Five Security, Microsemi, Micron Technology, NXP, Rambus, SecureRF Corporation, SiFive and Western Digital.



# Security Standing Committee: call to action!!

- Draft specs for: **cryptographic extensions to the RISC-V ISA**
- proposed approach to **trusted execution environments**
- ongoing collaborative work with
  - RISC-V privileged architecture,
  - vector extensions and instruction set compliance
  - ...
- With the advent of Spectre, Meltdown, TLBleed, Foreshadow, etc...
  - This is the right time to build **security** into **open source processors** from the start
  - RISC-V Foundation adopted specs are open and free
  - **Please join us and help us add security to RISC-V initiative!!**



# Security Standing Committee in the news

- A Press Release announcing the creation of the Security Standing Committee and mentioning 25 Member companies (17 companies listed by name) went out [July 2<sup>nd</sup> 2018](#) and is available on the Foundations website
- [“RISC-V Foundation Announces Security Standing Committee, Calls Industry To Join In Efforts”](#)
- Ted Speers, Microsemi and RISC-V Foundation Board Member posted an article on the RISC-V Security Standing Committee creation and history, July 24<sup>th</sup>, 2018
- <http://embedded-computing.com/author/ted-speers-risc-v-foundation-and-microsemi/>



<https://riscv.org/risc-v-foundation/>  
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