Call for Presentations

The open nature of, and both eco-system and community associated with RISC-V have arguably led to a "golden era" of research and innovation within the field of computer architecture. This, in turn, has positively impacted the associated field of hardware security, where significant existing challenges remain and new challenges continue to emerge. For example, use of RISC-V in this context offers opportunities (for academic and industrial research and development) which stem from the extensible, configurable nature of the ISA and many associated implementations, plus transparency afforded by access to HDL for many such implementations. Established in 2021 as a CHES affiliated event, the TASER workshop aims to 1) establish and solidify RISC-V as a topic of interest for CHES, and 2) act as an interface between the RISC-V and CHES communities. The 2022 workshop web-site is accessible at

https://ches.iacr.org/2022/taser

Again operating as a CHES affiliated event, and held in-person, the half-day workshop will include a mixture of invited and submitted presentations. As such, we invite submission of presentation proposals on topics including, but not limited to, the following:

- processor and co-processor RISC-V implementations,
- RISC-V optimised software implementation,
- security-oriented and performance oriented instruction set extensions,
- security-oriented verification,
- Trusted Execution Environments (TEEs) and related technologies,
- memory protection and control-flow integrity technologies,
- side-channel attacks and countermeasures.

Note that there will be no formal workshop proceedings; this is *not* a call for (full) papers, so proposals for presentations on 1) exploratory ideas or in-progress (versus completed) work, or 2) relevant hardware or software artefacts, tools, or techniques, would be reasonable.

Instructions for Authors

1. Prepare the presentation proposal. There is no template or similar, but, to simplify the review process, the proposal should be a 1-page PDF document which includes a list of authors (including an identified speaker), a title, an abstract, and a list of any references and/or associated resources.

2. Submit the presentation proposal via

   https://easychair.org/my/conference?conf=taser2022

   before the deadline, 11/06/22 23 : 59 : 59 Anywhere on Earth (AoE).

3. The organising committee will collectively review submitted proposals, considering both their individual technical merits plus constraints on and composition of the combined workshop program. We expect to notify authors of the outcome on 18/06/22.

Organising Committee

Benedikt Gierlichs	KU Leuven
Helena Handschuh	Rambus
Ben Marshall	PQShield
Richard Newell	Microchip
Daniel Page	University of Bristol