Call for Posters

Having been established in 1999, the Cryptographic Hardware and Embedded Systems (CHES) conference is the premier venue for research on both design and analysis of cryptographic hardware and software implementations. As an area conference of the International Association for Cryptologic Research (IACR), CHES bridges the cryptographic research and engineering communities, and attracts participants from academia, industry, government and beyond.

Following their success in previous events, the technical program at CHES 2025 will be supplemented by various additional activities. To this end, we invite proposals of interest for a poster presentation session to be held during the conference:

- At the informal poster presentation session, accepted author will display a prepared poster outlining ongoing work. This offers an ideal opportunity for discussing, getting feedback and engaging with the wider CHES community.
- Proposals are welcomed on any topic of broad interest to the CHES community.
- Rather than a finalized poster, the proposal should be a 1-page PDF clearly stating the following information:
  - title,
  - author(s), including name, affiliation, and email address,
  - description of the work that can include figures or references.
- Proposals (and/or any questions) should be submitted by email to the Poster Session Chair at

  chitchanok.chuengsatiansup@aau.at,

  - Submission: **7 July 2025**
  - Notification: 11 August 2025
- The notification of acceptance will specify any further logistical details required. However, note that the poster format is A0 portrait, and that authors are responsible for printing and bringing it to the poster session.

Poster Session Chair

Chitchanok Chuengsatiansup

University of Klagenfurt, Austria

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